

3571 U.S. PTO  
10/036505

01/07/02

1024

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10036505	FILING DATE 01/07/2002	CLASS 230 350	SUBCLASS 201.2	GAU 2852	EXAMINER Aiken
----------------------	---------------------------	---------------------	-------------------	-------------	-------------------

\*\*APPLICANTS: Kim Jeong;

\*\*CONTINUING DATA VERIFIED:

(none) TCC

\*\* FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-006107 01/15/2001

TCC

PG-PUB ☐ DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☒ yes ☐ no

35 USC 119 conditions met ☒ yes ☐ no

Verified and Acknowledged Examiners's initials

TCC TCC

ATTORNEY DOCKET NO

025311-0113

TITLE : Wafer edge exposure apparatus, and wafer edge exposure method

U.S. DEPT. OF COMM./PAT. & TM.-PTO-43EL (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs. Drwg.
Amount Due	Date Paid	Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Applicati n Examiner	
PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)